Project on
Electromagnetic Analysis, Design and Simulation of Dual Frequency
(S- and C-band) Relativistic Backward Wave Oscillator - A HPM Source

DRDO Sponsored Project (No.: R&D/SA/Electronics/DRDO/19-20/01)
DEPARTMENT OF ELECTRONICS ENGINEERING
INDIAN INSTITUTE OF TECHNOLOGY (BHU), VARANASI-221 005.

Advertisement for JRF position

Applications are invited from Indian nationals for the following position in the research project entitled
"Electromagnetic Analysis, Design and Simulation of Dual Frequency (S- and C-band) Relativistic
Backward Wave Oscillator - A HPM Source" sponsored by the Defence Research & Development
Organization (DRDO), Ministry of Defence, Government of India, New Delhi.

<table>
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<tr>
<th>Name of the Position</th>
<th>Junior Research Fellow (JRF)</th>
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<tr>
<td>Number of Position(s)</td>
<td>One (1)</td>
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<tr>
<td>Duration of the Position</td>
<td>One year. Extendable on performance basis</td>
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<td>Emoluments</td>
<td>Rs. 31,000 + HRA @ 16 % per month</td>
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<td>Educational Qualification</td>
<td>(i) B.E./B.Tech., with minimum of 55% marks in Electronics and Communication Engineering and having valid GATE score. (or) (ii) M.E./M.Tech., in Electronics Engineering (preferably RF &amp; Microwave Engineering) with minimum of 55% marks and GATE qualified. (iii) Experience in the area of RF and Microwave Engineering and related field is desirable.</td>
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<td>Upper Age limit as on the last date of application</td>
<td>28 years. Age relaxation applicable as per Government of India (GoI) rules.</td>
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General Terms and Conditions:
1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.

Application on Plain paper giving Name, Permanent and Correspondence address, Names of the Father and Mother, Telephone number. and e-mail address, details of educational career (starting from High School or equivalent) along with self-attested copies of all mark-sheets & certificates and details of any research or other experience etc., if any, should reach the office of the Principal Investigator by post or e-mail on or before 08.01.2022 by 5:00 PM.

Dr. M. Thottappan,
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DRDO Sponsored Research Project,
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