



भारतीय
प्रौद्योगिकी
संस्थान
काशी हिन्दू विश्वविद्यालय



INDIAN
INSTITUTE OF
TECHNOLOGY
BANARAS HINDU UNIVERSITY

Project Code No (of Institute): R&D/ECE/TTDF/24-25/32/677

Applications are invited from Indian nationals for the following position for the research project titled “**Empowering Sixth Sense Capabilities of Communication Network: Design and Development of Test-Bed for RIS-enabled Joint Sensing and Communication Systems**” sponsored by the Technology Development Fund (TTDF), an initiative under the Department of Telecommunications (DoT), Government of India.

1. **Name of the position:** Junior Research Fellow (JRF)/Senior Research Fellow (SRF).
2. **Number of positions:** 01
3. **Last date of Application:** Applicants should apply within 21 days of this advertisement.
4. **Emoluments (p.m.):** Rs 25000 – 42000 per month, based on experience.
5. **HRA:** as per norms
6. **Essential Educational Qualification:**

For Senior Research Fellow (SRF)

If the candidate has an M.Tech. degree:

- M.Tech. with First class in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.
- B.Tech with First class in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.
- 2 Years of research experience.

If the candidate has B.Tech. degree only:

- B.Tech with a First class in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.
- 2 Years of research experience.

For Junior Research Fellow (JRF)

- B.Tech with a First class in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.

7. Desirable qualification:

- **Strong knowledge of VHDL/Verilog** for the design and implementation of digital communication systems.
- **Strong knowledge of FPGA-based communication system design and implementation.**
- Hands-on experience with **AMD/Xilinx FPGA platforms**, including **ZCU702, ZCU102, ZCU208, and RFSoc** development boards.
- Proficiency in **AMD Vivado Design Suite, Vitis**, and FPGA debugging tools.

8. Age and Age Relaxation: As per Annexure IV.

Application Procedure:

Make a single PDF file having the filled application form (attached) with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate and others. Mail the PDF file to the following email ID: icclab.iitbhu@gmail.com and CC to atul.ece@iitbhu.ac.in with the subject line “**Empowering Sixth Sense Capabilities of Communication Network: Design and Development of Test-Bed for RIS-enabled Joint Sensing and Communication Systems**”.

Note: No hard copy is required. The applicant needs to present all original documents at the time of the interview, if shortlisted, based on the submitted data in the application.

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit based on qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be contacted to appear in the interview, and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately if selected.
5. No TA/DA will be paid for attending the interview.
6. Other relevant Guidelines of the funding agency and the GoI rules will be followed.

Please feel free to contact me in case of any queries:

Dr Atul Kumar (Principal Investigator),
Assistant Professor,
Department of Electronics Engineering,
Indian Institute of Technology (BHU)
Varanasi 221005 (U.P.) INDIA
Email ID: atul.ece@iitbhu.ac.in

Application Form

Personal details:

Name	
Father's name	
Mother's name	
E-mail	
Phone No.	
Date of Birth	
Category (GN/OBC/SC/ST)	
Permanent Address	
Correspondence Address	

Details of Education Qualification

Qualification	University/Board	Year of Passing	Percentage /CGPA	Subject
10 th				
12 th				
B.Tech/B.E				
M.Tech				
Others				

Details of qualifying examination (GATE, Net, or Others)

Name Of Examination	Subject of Examination	Year of Passing	Score	Marks Obtained

Details of Professional / Work Experience: At least current employment should be mentioned

Organization	Designation	Date-From	Date-To

The details furnished in this form are correct to my knowledge and belief.

Signature with Date and Name