

**Advertisement for JRF**  
**Department of Electrical Engineering**  
**IIT (BHU) Varanasi**

Applications are invited for FOUR posts of Junior Research Fellow (JRF) in a Defence Research and Development Organisation (DRDO), Government of India, sponsored project titled “**Compact Pulse power supply using sequentially controlled PFN based on Silicon carbide high gain modular converter**” sanctioned for a total duration of 3 years. The post is purely temporary and co-terminus with the project.

|  |   |
|--|---|
| Name of Position                       | Junior Research Fellow  |
| Number of Positions                    | 04  |
| Duration of the Position               | 3 years   |
| Emoluments                             | Rs. 37000/- for 1 <sup>st</sup> and 2 <sup>nd</sup> year and Rs. 42000/- for 3 <sup>rd</sup> Year (HRA @18% will be additional)   |
| Educational Qualification              | B.E./B.Tech. in the Electrical Engineering or any allied branches in First Division with GATE/NET<br>OR<br>M.E./M.Tech. in Electrical Engineering or any allied branches with First Division both at Graduate and Post Graduate level |
| Age Limit                              | Upper age limit – 28 years (5 years' relaxation for Female/SC/ST/ Physically Handicapped Candidate)   |
| Last date of receiving the application | Within 21 days of the advertisement   |

Note: The candidate may have the chance to pursue Ph.D. in Electrical Engineering while serving as a Junior Research Fellow (JRF) in the Electrical Engineering Department at IIT (BHU), Varanasi.

Application should be strictly according to the attached format and must reach **within 21 days** of the advertisement. Applicants can send their application along with self-attested copies of certificates, documents by Post/Email to the principal Investigator

Dr. Santosh K Singh  
Professor  
Department of Electrical Engineering  
IIT (BHU) Varanasi  
UP-221005, India  
Email: [sksingh.eee@iitbhu.ac.in](mailto:sksingh.eee@iitbhu.ac.in)

**General Instructions:**

1. Only shortlisted candidates will be communicated to appear for the interview.
2. Original certificates and self-attested copies of all certificates need to be presented for verification.
3. No TA/DA will be paid for attending interview

## APPLICATION FOR JRF POSITION

1. Full Name: PHOTO
2. Date of Birth:
3. Sex (M/F):
4. Category (GEN/SC/ST/OBC/EWS):
5. Marital Status:
6. Nationality:
7. Physically handicapped: Yes/No
8. Father's Name:
9. Mother's Name:
10. Address of Correspondence:
  - a. Mobile/Tel. No.:
  - b. E-mail Address:
11. Permanent address:
12. Academic Record:

a. Educational qualification:

| Examination passed | Name of School/<br>Institute /<br>University | Year of<br>passing | Subjects | Percentage of<br>marks/GPA obtained<br>/Division |
|--------------------|--|--------------------|----------|--|
| 10 <sup>th</sup>   |  |                    |          |  |
| 10+2 Sc            |  |                    |          |  |
| B.E./B.Tech        |  |                    |          |  |
| M.E./M.Tech        |  |                    |          |  |

b. GATE or Equivalent Exam Qualification details:

| GATE/<br>Equivalent | Year: | Qualified: Yes/No | Score: |
|---------------------|-------|-------------------|--------|
|                     |       |                   |        |

- c. Details of the project carried out with project title, place of work
- d. Attach list of publications (if any):
- e. Working experience (if any):
- f. Whether currently employed: Y/N
- g. Time required to join this position:

13. Names and addresses of two referees along with phone number and e-mail address.

### **Declaration**

I hereby declare that I have carefully read and understood the instructions and particulars for this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief.

Date:

Place:

Signature