



भारतीय
प्रौद्योगिकी
संस्थान
काशी हिन्दू विश्वविद्यालय



INDIAN
INSTITUTE OF
TECHNOLOGY
BANARAS HINDU UNIVERSITY

**Recruitment of Junior Research Fellow in the
Department of Electronics Engineering, IIT (BHU)**

Project Code No (Institute): R&D/IIT(BHU)SERB/ECE/19-20/02
SERB Code: SRG/2019/000573

Applications are invited from Indian nationals for the following position for the research project titled “**Development of Simulation Software for Spintronic Device and Circuit Simulation**” sponsored by Science and Engineering Board (SERB), Department of Science and Technology (DST), Government of India, New Delhi.

1. **Name of the position:** Junior Research Fellow (JRF)
2. **Number of positions:** 1
3. **Last date of Application:** Applicants should apply within 21 days of this advertisement.
4. **Emoluments (p.m.):** Rs. 31,000/- per month (Consolidated)
5. **Essential Educational qualification:**
 - i. M.Tech. with First class or B.Tech. with minimum 75% (7.5 CPI) in **Electronics and Communication Engineering** in the discipline/specialization concerned or in an allied discipline/area.
 - ii. B.Tech with First class in **Electronics and Communication Engineering**
6. **Desirable qualification:**
 - a. Knowledge of programming in python, c/c++and scripting languages like perl, and tcl/tk
 - b. GATE Qualified or JRF NET Qualified

Age: 31 years, As per latest Government of India (GoI) rules of JRF.

Age Relaxation: As per government of India Rules on producing the relevant certificate

Application Procedure:

Make a single PDF file having the filled application form (attached) with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate and others. Mail the PDF file to the following mail id "srg.iitbhu.2019@gmail.com" and CC to "shivam.ece@iitbhu.ac.in" with subject line "Application for the position of JRF in SERB Project".

Note: No hard copy is required. Applicant needs to show all the original documents at the time of the interview, if shortlisted, based on submitted data in the application.

General Terms and Conditions:

1. The position is purely temporary and is coterminous with the project.
2. The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.
6. Other relevant SERB Guidelines and Gol rules will be followed.

Please feel free to contact me in case of any query:

Dr. Shivam Verma (Principle Investigator),
Assistant Professor,
Department of Electronics Engineering,
Indian Institute of Technology (BHU)
Varanasi 221005 (U.P.) INDIA
Email ID: shivam.ece@iitbhu.ac.in

Application Form

Personal details:

Name	
Father's name	
Mother's name	
E-mail	
Phone No.	
Date of Birth and Category (GN/OBC/SC/ST/)	(certificate to be attached if OBC/SC/ST)
Permanent Address	
Correspondence Address	

Details of Education Qualification

Qualification	University/Board	Year of passing	Percentage /CGPA	Subject
10th				
12th				
B.Tech./B.E.				
M. Tech.				
Others				

Details of qualifying examination (GATE or NET if any)

Name of Examination	Subject of Examination	Year of Passing	Score	Marks Obtained

Details of Professional / Work Experience: At least current employment should be mentioned

Organization	Designation	Date-From	Date-To

The details furnished in this form are correct to the best of my knowledge and belief.

Signature with Date and Name